

PCI CORES

FAMILY TABLE

	PCI-T32	PCI-T32MF	PCI-M32	PCI-M32MF	PCI-HB	PCI-HB-AHB	PCI-DHB-AHB
PCI 33 MHz	✓	✓	✓	✓	✓	✓	✓
Host	✗	✗	✗	✗	✓	✓	✓
Master	✗	✗	✓	✓	✗	✗	✓
Target	✓	✓	✓	✓	✗	✗	✓
Multifunction	✗	✓	✗	✓	✗	✗	✗
32bit	✓	✓	✓	✓	✓	✓	✓
64bit	✗	✗	✗	✗	✗	✗	✗
SoC Interface	Generic AXI, AHB, Avalon-MM ¹	Generic AXI, AHB, Avalon-MM ¹	Generic AHB, AXI, Avalon-MM ¹	Generic AXI, AHB, Avalon-MM ¹	Generic AXI, Avalon-MM ¹	AHB	AHB
ASIC Support	✓	✓	✓	✓	✓	✓	✓
FPGA Support	✓ ²	✓ ²	✓ ²	✓ ²	✓ ²	✓ ²	✓ ²

- Notes:
- ✓ : Feature is supported
 - ✓ : Feature optionally supported
 - ✗ : Feature is not supported
 - 1 : SoC interface in grey font can be made available upon request
 - 2 : FPGA devices that do not support PCI I/O standard (e.g. Xilinx Virtex-6 and Stratix-V) are not supported

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I/O SUPPORT PER FPGA FAMILY

XILINX FAMILY	33MHz PCI ¹
Spartan-3	✓
Spartan-6	✓
Spartan-7	✓
Virtex-5	✓
Virtex-6	✗
Virtex-7	✓
Kintex-7	✓
Artix-7	✓
UltraScale	✗
UltraScale+	✗

Intel FPGA Family	33MHz PCI ¹
Cyclone-III	✓
Cyclone-IV	✓
Cyclone-V	✓
Cyclone 10 LP / GX	✓/✗
Stratix-III	✓
Stratix-IV	✓
Stratix-V	✗
Stratix-10	✗
Arria-V	✓
Arria-10	✗
Max-10	✓

Notes: ✓ : Family supports PCI I/O Standard
✗ : Family does not support PCI I/O Standard
1 : Evaluation for an exact part and package required