The AHB Compression Core Bus Bridge (CCBB) IP core implements a simple interface between an AMBA™ AHB bus and a Video or Image Compression Core available from CAST.

The CCBB-AHB provides an AHB slave interface for the compression core control/status interface, and a separate DMA-capable AHB master interface for the uncompressed pixel (stream-in) and compressed stream (stream-out) interfaces. Either or both the uncompressed pixel and the compressed stream paths can be removed to reduce the size and memory requirements of the CCBB-AHB core.

The core is available in RTL source code and targeted FPGA netlist format and it has been rigorously verified and proven in customer designs as well as in CAST reference designs.

### Applications

This Compression Core Bus Bridge core is suitable for facilitates compression core system integration for a wide range of applications, including surveillance and monitoring, video conferencing, video on demand, and other SD and HD video applications.

### Support

The CCBB-AHB as delivered is warranted against defects for ninety days from purchase. Thirty days of phone and email technical support are included, starting with the first interaction. Additional maintenance and support options are available.