

Codalip and CAST Collaboration Ships First Products to Customers

AUSTIN, TEXAS — Design Automation Conference — June 6, 2016 — Codalip, a leading provider of Application Specific Instruction-set Processor (ASIP) IP and tools, and CAST, a semiconductor intellectual property (IP) provider, announced today that the first CAST products created using Codalip's tools are now available to customers.

Codalip™ Studio, a comprehensive ASIP development environment, is able to generate a complete software development kit (SDK) optimized for a specific processor. CAST has used Codalip Studio to rapidly develop an improved verification environment, virtual system prototype, and full programming environment for the 16-bit [80251 Microprocessor IP core](#).

"Our popular 80251 IP had a functional but aging SDK. Thanks to the collaboration with Codalip, we are now delivering a state-of-the-art LLVM/GNU based SDK that helps designers get every ounce of performance and energy savings out of this MCS-51 compatible microcontroller," said Nikos Zervas, CAST's chief executive officer. "Excited by this success, we have already begun using Codalip technology for completely new products—both design IP and SDKs—and are eager to deliver additional optimized, competitively-superior processor solutions to our customers."

Using Codalip Studio, companies build a high-level model of a processor, then automatically generate from it everything needed by hardware and software development teams. This model-based approach drastically reduces development time and reduces the need for compiler experts or other dedicated resources, all without sacrificing quality or performance.

"CAST was an early supporter of our ASIP Design Network (ADN), and we are very pleased to see the first products from the collaboration come to market." said Karel Masarik, Codalip's chief executive officer. "Our unique technology allows customers to generate SDKs for existing processors or build new processors from scratch, and is used for our own Codix™ IP Cores. A model-based approach to processor design means new processors are generated with ease and can be rapidly customized to the exact needs of the customer."

Through its ASIP Design Network, Codasip encourages its partners to develop new and innovative technology built on top of Codasip's IP and ASIP Tools. The network includes IP creators, software companies, and service providers. More information in the ADN is available at <http://codasip.com/adn>.

About Codasip

Codasip delivers leading-edge technology that enables adoption of Application Specific Instruction-set Processors (ASIPs). ASIP's utilize dedicated instructions/architecture to accelerate software and are at the heart of applications that require very high performance with low power. Codasip's unique technology makes ASIP adoption as simple and easy as standard embedded processor cores. Formed in 2006 and headquartered in Brno, Czech Republic, Codasip currently has offices in the US and Europe. More information on Codasip's products and services is available at www.codasip.com.

About CAST

CAST is a twenty-three-year-old developer, integrator, and aggregator of digital IP cores for ASICs and FPGAs. The company offers some of the best available choices for low-power, high-value IP, including 8051s and BA2x 32-bit Processors; video, image, and data compression; automotive networking; peripherals, interfaces, security, and subsystems needed for complete system on chip designs. Visit www.cast-inc.com, or follow [@castcores](https://twitter.com/castcores) on Twitter.

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