

CAST Improves SDIO Host and NAND Flash Memory Controller Cores

DESIGNCON, Santa Clara, CA, February 3, 2009 — Silicon Intellectual Property (IP) provider CAST, Inc. today announced improvements to two solid state memory (SSD) controller cores:

- The **SDIO Host Controller Core** available now adds support for high-capacity SD, SDIO, and MMC memory cards; includes built-in content protection and encryption; and features Open Chip Protocol (OCP) or AMBA interfaces. See www.cast-inc.com/cores/sdio-host for details.
- An enhanced **NAND Flash Memory Controller Core** available next month includes 4-, 8-, and now 16-bit error correction with the Bose, Chaudhuri, and Hocquenghem (BCH) algorithm—critical for Multi Level Cell (MLC) memories—and features ONFi standard compliance and an OCP interface. See www.cast-inc.com/cores/nandflash-ctrl for details.

The new SDIO and NAND Flash controller cores were developed by long-time CAST partner Evatronix SA in Poland. They are part of CAST's memory and storage controller IP family, which also includes DDR, DDR2, SDRAM, and ATE/IDE IP cores. The competitive speed, area, and low-power characteristics of these cores have made them suitable for a variety of customer applications, including aerial reconnaissance, cellular phones, mobile multimedia devices, and wireless sensors.

About CAST, Inc.

CAST provides a broad range of popular IP cores and system IP for ASICs and FPGAs. Privately owned and operating since 1993, CAST has a reputation for high-quality IP products and industry-leading technical support. The company is headquartered near New York City, partners with IP developers around the world, and works with select sales consultants and distributors throughout Europe and Asia. Learn more at www.cast-inc.com.

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