

CAST Offers PCI Express Model, Highlights APS 32-bit Processor Cores at DAC 2007

Design Automation Conference (DAC), San Jose, California, June 4, 2007 — Semiconductor intellectual property (IP) provider CAST, Inc. today announced a new downloadable free simulation model of its PCI Express IP core, and revealed details of a DAC demonstration and promotion for its APS 32-bit processor cores.

The PCIe Core Backend Model (CBM) Demo of the CAST CPXP-EP PCI Express Endpoint Controller allows a designer to simulate a system with the core at the transaction layer level. Prepared test cases and scripts and an included design example illustrate the features and functions of a PCIe endpoint controller, and multiple parameters and the ability to program a custom application facilitate experimentation with and learning of this popular interface technology. The PCIe CBM Demo is free to qualified designers after on-line registration at www.pciexpresscores.com.

The APS family of 32-bit processor cores includes compact, high-performance processors aimed at 8051 upgrade applications, plus a recently-announced DSP coprocessor. Attendees who stop by CAST's DAC booth to learn more about APS and get a demo can try their hand at an amusing slot machine controller application of the APS3 processor core. Those with a winning pull of the virtual lever will receive an Apple iPod Shuffle in the color of their choice.

CAST is exhibiting in booth 5363 at DAC in San Diego, June 1–4. Details on the company's 100+ IP cores and SoC Kernels IP subsystems will be available; learn more at www.cast-inc.com.

About CAST, Inc.

CAST provides over 100 popular and standards-based IP cores for ASICs and FPGAs. Privately owned and operating since 1993, CAST has established a reputation for high-quality IP products, simple licensing, and responsive technical support.

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*Contacts: Hal Barbour, CAST, Inc., +1 (201) 391-8300 ext. 111, h.barbour@cast-inc.com
Paul Lindemann, Montage Marketing, +1 (603) 490-4985, paul@montmark.com*

*CAST, Inc. 11 Stonewall Court, Woodcliff Lake, NJ 07677 Tel: 201/391-8300 Fax: 201/391-8694 www.cast-inc.com
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