



News Release

EDITORIAL/READER CONTACT:
Brian Kiernan, Corporate Communications Manager
Lattice Semiconductor Corporation
503-268-8739 voice
503-268-8193 fax
brian.kiernan@latticesemi.com

Hal Barbour, President
CAST Incorporated
201-391-8300 voice
201-391-8694 fax
hal@cast-inc.com

INTELLECTUAL PROPERTY SUPPORT FOR NEW FPGA FAMILIES ANNOUNCED BY LATTICE SEMICONDUCTOR AND CAST INCORPORATED

*-IP Cores Implement CAN Bus and IEEE 1394 Protocols
Commonly Found In High-Volume Systems-*

HILLSBORO, OR / WOODCLIFF LAKE, NJ – SEPTEMBER 2, 2004 – Lattice Semiconductor Corporation (NASDAQ: LSCC) and its IP partner, CAST Incorporated, today announced intellectual property (IP) support for the new LatticeECP-DSP™ (“Economy Plus DSP”) and LatticeEC™ (“Economy”) FPGA device families. Through the Lattice ispLeverCORE™ Connection partner program, CAST will provide a range of complete system solutions for their customers who are integrating system-level IP with the most advanced silicon architectures from Lattice. The Lattice partners program is designed to allow customers to easily access and integrate approved third-party IP products using Lattice programmable devices.

“IP support for our new FPGA families continues to grow at an impressive rate. The combination of the CAST CAN Bus Controller and IEEE 1394 IPs with the optimized

LatticeECP™ and EC architectures is an excellent approach to a variety of high-volume, cost-sensitive applications,” said Stan Kopec, Lattice vice president of corporate marketing. “CAST is uniquely positioned to provide IP supporting these serial bus standards in the latest generation Lattice devices,” Kopec concluded.

Hal Barbour, President of CAST Incorporated said, “The new LatticeECP and LatticeEC devices, and associated ispLEVER® tools, represent a pronounced enhancement to the previous generation of Lattice FPGAs; we are excited to implement these key IP cores for the Lattice low-cost devices.”

LatticeECP and LatticeEC Cores from CAST

CAST has ported, optimized and tested two popular IP cores on the new Lattice FPGA devices, the IEEE 1394a (FireWire) Link Layer Controller and the CAN Bus Controller, with competitive implementation results:

CAST IP Available for LatticeECP & LatticeEC

CAST Product	LUTs	Speed
CAN: CAN Bus Controller	1619	>40Mhz
C1394A: IEEE1394a Link Layer Controller	6371	>400Mbps

Full datasheets for these cores are available on the Lattice Web site:

<http://www.latticesemi.com/products/devtools/ip/cast/index.cfm>

These Connection Cores are available in netlist format for immediate purchase from CAST. CAST also offers a variety of additional packaging and licensing options to suit specific customer needs. By leveraging partner products, customers can quickly implement a wide variety of functions in Lattice programmable devices.

About the LatticeECP-DSP and LatticeEC FPGA Families

Announced June 28, 2004, the LatticeECP-DSP and LatticeEC FPGA device families are architected to provide the most optimized feature sets combined with the lowest total solution costs of any FPGAs. The new LatticeECP-DSP products, targeted for high-performance DSP applications, provide up to a 50% performance and 75% logic utilization improvement over other low-cost solutions when implementing common DSP functions. The LatticeEC FPGA product family, targeted for general-purpose FPGA applications, is a precise and targeted response to the

market's explosive demand for low-cost, architecturally streamlined logic solutions. Through advanced 130nm silicon technology, an optimized architecture and proprietary circuit design, the new Lattice devices lower total solution costs by up to 30% to 50% compared with existing FPGA solutions, and are expected to broaden the adoption of FPGAs within the \$20 billion ASIC marketplace.

About CAST Incorporated

CAST provides about 100 popular and standards-based IP cores for ASICs and FPGAs. Privately owned and operating since 1993 with a focus on making IP practical and affordable, CAST has established a reputation for high-quality IP products, simple licensing, and responsive technical support. The company is headquartered near New York City, and works with an international network of IP developers and distributors. Company headquarters are located at 11 Stonewall Court, Woodcliff Lake, NJ 07677, USA; Telephone 201-391-8300; FAX 201-391-8694. For more information about CAST, visit <http://www.cast-inc.com/>

About Lattice Semiconductor

[Lattice Semiconductor Corporation](#) designs, develops and markets the broadest range of [Field Programmable Gate Arrays \(FPGA\)](#), [Field Programmable System Chips \(FPSC\)](#) and high-performance ISP™ [Programmable Logic Devices \(PLD\)](#), including [Complex Programmable Logic Devices \(CPLD\)](#), [Programmable Analog Chips \(PAC™\)](#), and [Programmable Digital Interconnect \(GDX™\)](#). Lattice also offers industry leading [SERDES](#) products. Lattice is "Bringing the Best Together" with comprehensive solutions for today's system designs, delivering innovative programmable silicon products that embody leading-edge system expertise.

Lattice products are sold worldwide through an extensive network of independent sales representatives and distributors, primarily to OEM customers in the fields of communications, computing, computer peripherals, instrumentation, industrial controls and military systems. Company headquarters are located at 5555 NE Moore Court, Hillsboro, Oregon 97124-6421, USA; telephone 503-268-8000, fax 503-268-8037. For more information about Lattice Semiconductor Corporation, visit <http://www.latticesemi.com>.

Statements in this news release looking forward in time are made pursuant to the safe harbor provisions of the Private Securities Litigation Reform Act of 1995. Investors are cautioned that forward-looking statements involve risks and uncertainties including market acceptance and demand for our new products, our dependencies on our silicon wafer and intellectual property suppliers, the impact of competitive products and pricing, technological and product development risks and other risk factors detailed in the Company's Securities and Exchange Commission filings. Actual results may differ materially from forward-looking statements.

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