Cores arrive in a dozen

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Xilinx's IP Center is offering 12 new cores for its Virtex and Spartan series FPGAs that target a wide spectrum of applications, ranging from modems and channel coding, to video and PowerPC-based designs. The cores are available from four Xilinx AllianceCore partners. One partner, Cast (www.cast-inc.com), is providing two types of 8051 microcontrollers, plus the 8254 programmable interval timer/counter and an 8250 UART core. Another partner, Cselt (www.cselt.it), is providing an interleaver/deinterleaver core for channel coding, a forward-inverse-DCT core for image processing, an IP-content-addressable-memory core, and a network-data parser core for networking-router applications. Meanwhile, partner Deltatec (www.deltatec.be) plans to provide a longitudinal-time-code-generator core for industrial- and broadcast-video applications. Eureka Technology (www.eurekatech.com) is providing both a PowerPC bus-master core and a slave-controller core for PowerPC-peripheral applications and an SDRAM controller core.


-by Gabe Moretti